of the external light, are circular. One dummy opening **190** is provided for one sub pixel **144**; in other words, one dummy opening **190** is located between every two sub pixels **144** adjacent in a vertical direction, and one dummy opening **190** is located between every two sub pixels **144** adjacent in a horizontal direction. It should be noted that the shape, positions and the number of the dummy openings **190** are not limited to those in this example.

The dummy openings **190** in this example may be used in any of sub pixel array examples 1 through 9.

Another Embodiment

In another embodiment according to the present invention, the size of an area demarcated by the bank **130** may be approximately the same, or larger than, a total size of the openings, corresponding to the area, of the light blocking layer.

This structure allows light to be extracted from most of the light emitting regions. This provides an effect of realizing a display device having a higher efficiency.

As shown in FIG. **3**, the bank **130** covers the peripheral area of the individual pixel electrode **124**. The light emitting layer **128** and the common pixel electrode **126** are provided on a top surface of the individual pixel electrode **124** and a top surface of the bank **130**. In the light emitting element **120**, a region where the individual pixel electrode **124**, the light emitting layer **128** and the common pixel electrode **126** overlap each other may be regarded as a light emitting region. The bank **130** isolates the light emitting layer **128** and the common pixel electrode **126** from the individual pixel electrode **124** at the peripheral area of the individual pixel electrode **124**. Because of this structure, the bank **130** may be regarded as demarcating the light emitting region.

The bank **130** may be formed of an organic material such as, for example, an acrylic resin or a polyimide resin, or an inorganic material such as, for example, SiO_2 or SiN.

The bank **130** in this embodiment may be usable in any of sub pixel array examples 1 through 9.

The display device **100** in a preferable embodiment according to the present invention has been described. In this embodiment, the display device **100** providing high definition display is provided.

The above-described embodiments and examples are merely examples, and the technological scope of the present invention is not limited to any of the above-described embodiments or examples. A person of ordinary skill in the art would make various alterations without departing from the gist of the present invention. Therefore, such alterations are to be construed to be encompassed in the technological scope of the present invention.

The invention claimed is:

1. A display device, comprising:

a substrate;

a pixel including a plurality of sub pixels each including a light emitting region, the pixel being provided on the substrate;

a bank demarcating the plurality of sub pixels and exposing the light emitting regions; and

a light blocking layer located on an opposite side of the pixel from the substrate;

wherein:

the light emitting regions are L-shaped; and

the light blocking layer covers at least a bending portion of each of the light emitting regions and divides each of the light emitting regions into a plurality of regions, the bending portion making the light emitting regions L-shaped.

2. The display device according to claim **1**, wherein:

the sub pixels included in the pixel are four rectangular sub pixels located in a matrix of two rows by two columns; and

each light emitting region included in each of the four sub pixels is located so as to have the bending portion facing a corner of the rectangular sub pixel, and is located along a part of an outer peripheral edge of the pixel.

3. The display device according to claim **1**, wherein:

the sub pixels included in the pixel are four rectangular sub pixels located in a matrix of two rows by two columns;

the four sub pixels include:

a first sub pixel and a second sub pixel located on a first diagonal line of the pixel; and

a third sub pixel and a fourth sub pixel located on a second diagonal line of the pixel different from the first diagonal line;

a first light emitting region of the first sub pixel and a second light emitting region of the second sub pixel each include:

a first region extending in a first direction from an intersection of the first sub pixel and the second sub pixel;

the bending portion located at an end of the first region, the end being opposite to the intersection; and

a second region extending in a second direction crossing the first direction from the bending portion along an outer peripheral edge of the pixel; and

a third light emitting region of the third sub pixel and a fourth light emitting region of the fourth sub pixel each include:

a third region extending in the second direction from the intersection;

the bending portion located at an end of the third region, the end being opposite to the intersection; and

a fourth region extending in the first direction from the bending portion along the outer peripheral edge of the pixel.

4. The display device according to claim **1**, further comprising:

a counter substrate facing the substrate; and

a color filter and the light blocking layer provided on the counter substrate;

wherein the light blocking layer is located between the color filter and the counter substrate.

5. The display device according to claim **1**, wherein the light emitting region is divided into two line-symmetrical regions.

6. The display device according to claim **1**, wherein the light blocking layer has openings at positions not overlapping the light emitting regions.

7. The display device according to claim **1**, wherein:

the sub pixels each include a pixel electrode, a light emitting layer provided on the pixel electrode, and a common electrode provided on the light emitting layer; and

the pixel electrode overlaps the plurality of regions divided by the light blocking layer.

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